

# ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention	METHOD AND APPARATUS FOR BORDER NODE BEHAVIOR ON A FULL DUPLEX BUS
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Application Number : 10/635706



Confirmation Number: 8329

First Named Applicant: Jerryold Hauck

Attorney Docket Number:

Art Unit:

Examiner:

Search string: ( 5175855 or 5274631 or 5321812 or 5343461 or 5390301 or 5394106 or 5394486 or 5406643 or 5430486 or 5490250 or 5493568 or 5524254 or 5559548 or 5563886 or 5568487 or 5632016 or 5642515 or 5654657 or 5701492 or 5706278 or 5754765 or 5764930 or 5802365 or 5805822 or 5819115 or 5826027 or 5844619 or 5923663 or 5930480 or 5935208 or 5940600 or 5954796 or 5991842 or 6009480 or 6038234 or 6041286 or 6091726 or 6115764 or 6118486 or 6131134 or 6131163 or 6138163 or 6145018 or 6178445 or 6185622 or 6188700 or 6192397 or 6199119 or 6212171 or 6212633 ).pn

## US Patent Documents

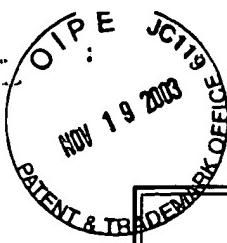
Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
MJ	1	5175855	1992-12-29	Putnam et al	A	395	700
	2	5274631	1993-12-28	Bhardwaj	A	370	60
	3	5321812	1994-06-14	Benedict et al	A	395	200
	4	5343461	1994-08-30	Barton et al	A	370	13
	5	5390301	1995-02-14	Scherf	A	395	325
	6	5394106	1995-02-28	Black et al	A	327	107
	7	5394486	1995-02-28	Eisenbarth et al	A	382	57
	8	5406643	1995-04-11	Burke et al	A	395	200
	9	5430486	1995-07-04	Fraser et al	A	348	426
	10	5490250	1996-02-06	Reschke et al	A	375	185.01
	11	5493568	1996-02-20	Sampat et al	A	370	60
	12	5524254	1996-07-04	Morgan et al	A	395	800
	13	5559548	1996-09-24	Davis et al	A	348	6
	14	5563886	1996-10-08	Kawamura et al	A	370	94.3
	15	5568487	1996-10-22	Sitbon et al	A	370	94.1
	16	5632016	1997-05-20	Hoch et al	A	395	200.02
↓	17	5642515	1997-06-24	Jones et al	A	395	727
MJ	18	5654657	1997-08-05	Pearce	A	327	163

MJ	19	5701492	1997-12-23	Wadsworth et al	A	395	712
/	20	5706278	1998-01-06	Robillard et al	A	370	222
/	21	5754765	1998-05-19	Danneels et al	A	395	200.1
	22	5764930	1998-06-09	Staats	A	395	287
	23	5802365	1998-09-01	Kathail et al	A	395	681
	24	5805822	1998-09-08	Long et al	A	395	200.62
	25	5819115	1998-10-06	Hoeze et al	A	395	888
	26	5826027	1998-10-20	Pedersen et al	A	395	200.51
	27	5844619	1998-12-01	Songer	A	348	447
	28	5923663	1999-07-13	Bontemps et al	A	370	445
	29	5930480	1999-07-27	Staats	A	395	200.74
	30	5935208	1999-08-10	Duckwall et al	A	709	221
	31	5940600	1999-08-17	Staats et al	A	395	287
	32	5954796	1999-09-21	McCarty et al	A	709	222
	33	5991842	1999-11-23	Takayama	A	710	105
	34	6009480	1999-12-28	Pleso	A	710	8
	35	6038234	2000-03-14	LaFollette et al	A	370	443
	36	6041286	2000-03-21	White	A	702	176
	37	6091726	2000-07-18	Crivellari et al	A	370	392
	38	6115764	2000-09-05	Chisholm et al	A	710	100
	39	6118486	2000-09-12	Reitmeier	A	348	441
	40	6131134	2000-10-10	Huang et al	A	710	103
	41	6131163	2000-10-10	Wiegel	A	713	201
	42	6138163	2000-10-24	Nam et al	A	709	231
	43	6145018	2000-11-07	LaFollette et al	A	710	8
	44	6178445	2001-01-23	Dawkins et al	B1	709	209
	45	6185622	2001-02-06	Sato	B1	709	233
	46	6188700	2001-02-13	Kato et al	B1	370	477
	47	6192397	2001-02-20	Thompson	B1	709	209
	48	6199119	2001-03-06	Duckwall et al	B1	710	8
↓	49	6212171	2001-04-03	LaFollette et al	B1	370	257
MJ	50	6212633	2001-04-03	Levy et al	B1	713	153

### Signature

Examiner Name	Date
<i>Min Tang</i>	3/16/06



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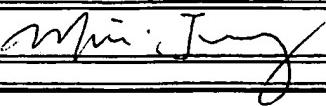
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Title of Invention	METHOD AND APPARATUS FOR BORDER NODE BEHAVIOR ON A FULL-DUPLEX BUS						
Application Number:	10/635706						
Confirmation Number:	8329						
First Named Applicant:	Jerrold Hauck et al.						
Attorney Docket Number:	APPL-P2834DVB						
Search string:	( 5406643 or 5524254 or 5642515 or 5654657 or 5706278 or 5719862 or 5754765 or 5802365 or 5826027 or 5845152 or 5923663 or 5930480 or 5935208 or 5940600 or 5954796 or 5991842 or 6032261 or 6038234 or 6091726 or 6115764 or 6131134 or 6145018 or 6199119 or 6212171 or 6212633 or 6219697 or 6233615 or 6233624 or 6243778 or 6247063 or 6266344 or 6275889 or 6292840 or 6314461 or 6343321 or 6347362 or 6356558 or 6363085 or 6373821 or 6405247 or 6411628 or 6418150 or 6425019 or 6442630 or 6446142 or 6452975 or 6457086 or 6466982 or 6496862 or 6519657 ).pn.						
<b>US Patent Documents</b>							
Note: Applicant is not required to submit a paper copy of cited US Patent Documents							
init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
MJ	1	5406643	1995-04-11	Burke et al.	A	395	200
	2	5524254	1996-06-04	Morgan	A	395	800
	3	5642515	1996-06-24	Jones et al.	A	395	727
	4	5654657	1997-08-24	Pearce	A	327	163
	5	5706278	1998-01-06	Robbillard et al.	A	370	222
	6	5719862	1998-02-17	Lee	A	370	355
	7	5754765	1998-05-19	Danneels et al.	A	395	200.1
	8	5802365	1998-09-01	Kathail et al.	A	395	681
	9	5826027	1998-10-20	Pedersen et al.	A	395	200.51
	10	5845152	1998-12-01	Anderson et al.	A	395	872
U	11	5923663	1999-07-13	Bontemps et al.	A	370	455
MJ	12	5930480	1999-07-27	Staats	A	395	200.74

MJ	13	5935208	1999-08-10	Duckwall et al.	A	709	221
	14	5940600	1999-08-17	Staats et al.	A	710	107
	15	5954796	1999-09-21	McCarty et al.	A	709	222
	16	5991842	1999-11-23	Takayama	A	710	105
	17	6032261	2000-02-29	Hulyalkar	A	713	400
	18	6038234	2000-03-14	LaFollette et al.	A	370	443
	19	6091726	2000-07-18	Crivellari	A	370	392
	20	6115764	2000-09-05	Chisholm et al.	A	710	100
	21	6131134	2000-10-10	Huang et al.	A	710	103
	22	6145018	2000-11-07	LaFollette et al.	A	710	8
	23	6199119	2001-03-06	Duckwall et al.	B1	710	8
	24	6212171	2001-04-03	LaFollette et al.	B1	370	257
	25	6212633	2001-04-03	Levy et al.	B1	713	153
	26	6219697	2001-04-17	Lawande et al.	B1	709	221
	27	6233615	2001-05-15	Van Loo	B1	709	224
	28	6233624	2001-05-15	Hyder et al.	B1	709	327
	29	6243778	2001-06-05	Fung et al.	B1	710	113
	30	6247063	2001-06-12	Ichimi et al.	B1	709	250
	31	6266344	2001-07-24	Fujimori et al.	B1	370	468
	32	6275889	2001-08-14	Saito	B1	710	129
	33	6292840	2001-09-18	Blomfield-Brown et al.	B1	709	247
	34	6314461	2001-11-06	Duckwall et al.	B1	709	221
	35	6343321	2002-01-29	Patki et al.	B2	709	227
	36	6347362	2002-02-12	Schoinas et al.	B1	711	147
	37	6356558	2002-03-12	Hauck et al.	B1	370	450
	38	6363085	2002-03-26	Samuels	B1	370	502
	39	6373821	2002-04-16	Staats	B1	370	252
	40	6405247	2002-06-11	Lawande et al.	B1	709	221
	41	6411628	2002-06-25	Hauck et al.	B1	370	447
	42	6418150	2002-07-09	Staats	B1	370	503
	43	6425019	2002-07-23	Tateyama et al.	B1	710	11
	44	6442630	2002-08-27	Takayama et al.	B1	710	105
	45	6446142	2002-09-03	Shima et al.	B1	710	16
	46	6452975	2002-09-17	Hannah	B1	375	257
	47	6457086	2002-09-24	Duckwall	B1	710	305
	48	6466982	2002-10-15	Ruberg	B1	709	227
MJ	49	6496862	2002-12-17	Akatsu et al.	B1	709	224
MJ	50	6519657	2003-12-11	Stone et al.	B1	710	10

**Signature**

Examiner Name	Date
	3/16/06

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Title of Invention	METHOD AND APPARATUS FOR BORDER NODE BEHAVIOR ON A FULL DUPLEX BUS
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Application Number : 10/635706



Confirmation Number: 8329

First Named Applicant: Jerryold Hauck

Attorney Docket Number:

Art Unit:

Examiner:

Search string: ( 6295479 or 6353868 or 6426062 or 6529552 or 6587904 or 6591300 or 6618750 or 6618785 or 6628607 or 20010001151 or 20010019561 or 20010024423 or 20020101231 or 20020103947 or 20020188780 or 20020188783 or 20030037161 or 20030055999 ).pn

## US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
MJ	1	6295479	2001-09-25	Shima et al.	B1	700	83
	2	6353868	2003-10-17	Takayama et al.	B1	710	129
	3	6426062	2002-07-30	Chopra et al.	B1	424	65
	4	6529552	2003-03-04	Ito et al.	B1	370	466
	5	6587904	2003-07-01	Hauck et al.	B1	710	107
	6	6591300	2002-07-08	Yurkovic	B1	709	226
	7	6618750	2003-09-09	Staats	B1	709	209
V	8	6618785	2003-09-09	Whitby-Strevens	B1	710	305
MJ	9	6628607	2003-09-09	Hauck et al.	B1	370	216

## US Published Applications

Note: Applicant is not required to submit a paper copy of cited US Published Applications

init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
MJ	1	20010001151	2001-05-10	Duckwall et al.	A1	710	8
	2	20010019561	2001-09-06	Staats	A1	370	487
	3	20010024423	2001-09-27	Duckwall et al	A1	370	254
V	4	20020101231	2002-08-01	Staats	A1	324	126
MJ	5	20020103947	2002-08-01	Duckwall et al.	A1	710	19

6	20020188780	2002-12-12	Duckwall	A1	710	105
7	20020188783	2002-12-12	Duckwall et al	A1	710	119
8	20030037161	2003-02-20	Duckwall et al	A1	709	233
9	20030055999	2003-03-20	Duckwall et al	A1	709	236

**Signature**

Examiner Name	Date
M. Tj	3/16/06



Page 1 of 1

Form 1449 (Rev. 2-32) U.S. Department of Commerce Patent and Trademark Office					Atty. Docket No. APPL-P2834DVB	Serial No. 10/635,706		
<b>Information Disclosure Statement by Applicant</b>					Applicant: Jerrold V. Hauck et al.			
(Use several sheets if necessary)					Filed: August 5, 2003 Group: 2661			
<b>U.S. Patent Documents</b>								
Init.		Document No.	Date	Name	Class	Subclass	Filing Date	
<i>MJ</i>	1	6,426,962 B1	07/30/2002	Cabezas et al.	370	516	06/24/1998	
<b>Foreign Documents</b>								
Translation								
Init.		Document No.	Date	Country	Class	Subclass	Yes      No	
<i>MJ</i>	2	EP 1 085 706 A2	03/21/2001	Europe	H04L	12/46		X
<i>MJ</i>	3	EP 1 085 706 A3	10/30/2002	Europe	H04L	12/46		X
<b>Other Documents (Including Author, Title, Date, Pertinent Pages, etc.)</b>								
<i>MJ</i>	4	Bregni et al., "Jitter Testing Technique and Results at VC-4 Desynchronizer Output of SDH Equipment, <u>IEEE International Conference on Communications</u> , Vol. 3, pp. 1407-1410, May 12, 1994.						
<i>MJ</i>	5	'Information technology-Microprocessor systems-Control and Status registers (CSR) Architecture for microcomputer buses', [ANSI/IEEE Std 1212, 1994 Ed.], <u>The Institute of Electrical and Electronics Engineers, Inc.</u> , pp. 1-122, October 5, 1994.						
<i>MJ</i>	6	Bregni et al., "Jitter Testing Technique and Results at VC-4 Desynchronizer Output of SDH Equipment, <u>IEEE Transactions on Instrumentation and Measurement</u> , Vol. 44, Issue 3, pp. 675-678, June 1995.						
<i>MJ</i>	7	Shiwen et al., "Parallel positive justification in SDH C-4 mapping, <u>IEEE International Conference on Communications</u> , Vol. 3, pp. 1577-1581, June 12, 1997						
<i>MJ</i>	8	'Information Technology-Fibre Channel-Methodologies for Jitter Specification', <u>NCITS Technical Report (NCITS TR-25-1999, REV 10)</u> , pp. 1-96, June 9, 1999.						
Examiner <i>MJ</i>				Date Considered <u>3/16/06</u>				
Examiner: Initial if citation considered, whether or not citation is in conference with MPEP 609; Draw line through citation if not conformance and not considered. Include a copy of this form with the next communication to applicant.								



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	1	6295479	2001-09-25	Shima et al.	B1	700	83							
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	4	6529552	2003-03-04	Ito et al.	B1	370	466							
	5	6587904	2003-07-01	Hauck et al.	B1	710	107							
	6	6591300	2002-07-08	Yurkovic	B1	709	226							
	7	6618750	2003-09-09	Staats	B1	709	209							
	8	6618785	2003-09-09	Whitby-Strevens	B1	710	305							
	9	6628607	2003-09-09	Hauck et al.	B1	370	216							
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	1	20010001151	2001-05-10	Duckwall et al.	A1	710	8							

2	20010019561	2001-09-06	Staats	A1	370	487
3	20010024423	2001-09-27	Duckwall et al	A1	370	254
4	20020101231	2002-08-01	Staats	A1	324	126
5	20020103947	2002-08-01	Duckwall et al.	A1	710	19
6	20020188780	2002-12-12	Duckwall	A1	710	105
7	20020188783	2002-12-12	Duckwall et al	A1	710	119
8	20030037161	2003-02-20	Duckwall et al	A1	709	233
9	20030055999	2003-03-20	Duckwall et al	A1	709	236

Signature

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*duplicate*